

Figure 1

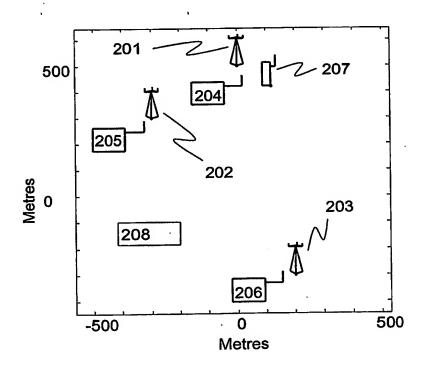


Figure 2

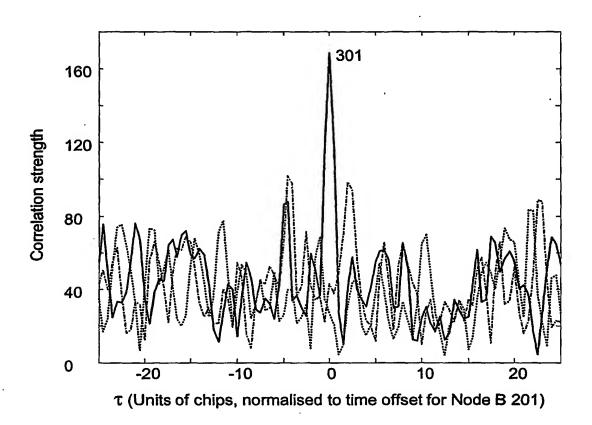


Figure 3

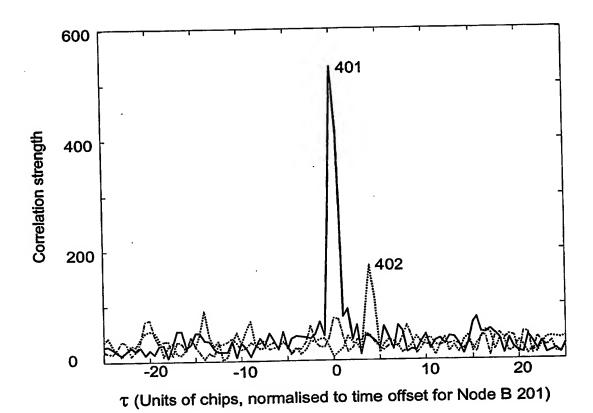


Figure 4

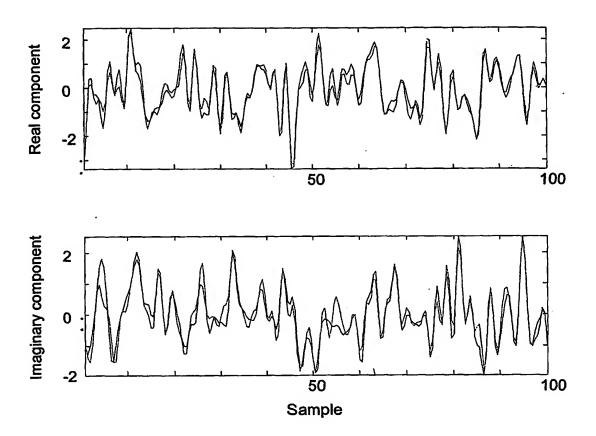
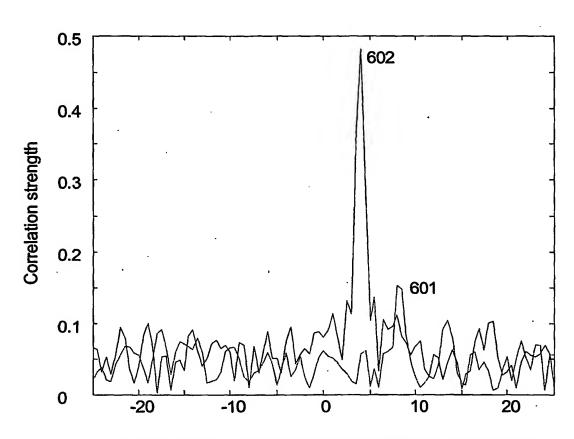


Figure 5



 τ (Units of chips, normalised to time offset for Node B 201)

Figure 6

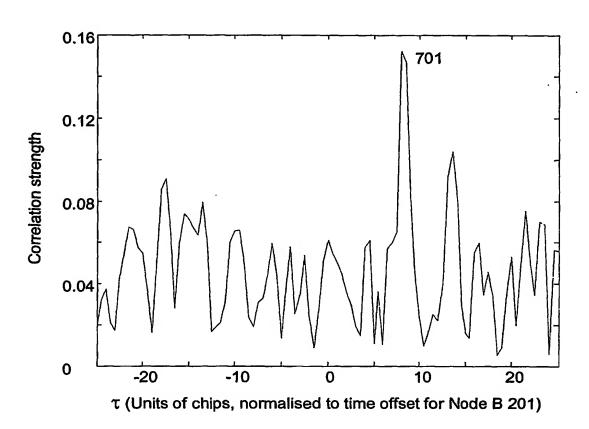


Figure 7

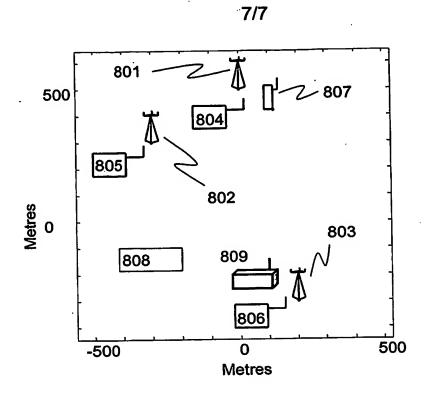


Figure 8